

Roll No. 

--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--

Total No. of Pages : 02

Total No. of Questions : 07

**BCA (Sem – 2 )**  
**COMPUTER SYSTEM ARCHITECTURE**  
Subject Code : UGCA-1908  
M.Code : 77416  
Date of Examination : 06-07-22

Time : 3 Hrs.

Max. Marks : 60

**INSTRUCTIONS TO CANDIDATES :**

1. SECTION-A is COMPULSORY consisting of TEN questions carrying TWO marks each.
2. SECTION-B contains SIX questions carrying TEN marks each and students have to attempt any FOUR questions.

**SECTION-A**

**Q1. Write briefly :**

- a) Define NOR gate.
- b) What is meant by Truth Table?
- c) What is POS form?
- d) What are K-maps?
- e) What is meant by full adder?
- f) Name two uses of demultiplexer.
- g) Define an Encoder.
- h) What is CISC?
- i) What is meant by register transfer?
- j) Differentiate Control bus and Address bus.

**SECTION-B**

**Q2. Generate AND, OR and NOT gates using NOR gates.**

**Q3. a) Discuss the working of Full Subtractor Circuit.**

**b) What is meant by Decoder? Explain.**

- Q4. What is meant by SR Flip Flop? Explain the race-around condition in detail.
- Q5. Explain the Harvard Architecture.
- Q6. a) What are register reference instructions? Explain.  
b) Explain the working of D-Flip Flop.
- Q7. Draw and explain the data movement among registers using common bus.

**NOTE : Disclosure of Identity by writing Mobile No. or Making of passing request on any page of Answer Sheet will lead to UMC against the Student.**