



- Q4. What is meant by SR Flip Flop? Explain the race-around condition in detail.
- Q5. Explain the Harvard Architecture.
- Q6. a) What are register reference instructions? Explain.  
b) Explain the working of D-Flip Flop.
- Q7. Draw and explain the data movement among registers using common bus.

**NOTE : Disclosure of Identity by writing Mobile No. or Making of passing request on any page of Answer Sheet will lead to UMC against the Student.**