

Roll No.

--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--

Total No. of Pages : 02

Total No. of Questions : 16

B.Sc. (IT) (2019 Batch) (Sem.-1)
COMPUTER SYSTEM ARCHITECTURE
Subject Code : UGCA-1908
M.Code : 76954

Time : 3 Hrs.

Max. Marks : 60

INSTRUCTIONS TO CANDIDATES :

1. SECTION-A is COMPULSORY consisting of TEN questions carrying TWO marks each.
2. SECTION-B contains SIX questions carrying TEN marks each and students have to attempt any FOUR questions.

SECTION-A

Write briefly :

1. How NOR gate is used as Universal Gate?
2. Write the truth table of SR Flip Flop.
3. Draw a 1*4 de-multiplexer.
4. Write use of Karnaugh Maps.
5. What is POS form?
6. Write a short note on encoders.
7. Discuss Harvard Architecture.
8. What are the applications of Flip Flop?
9. What are micro operations?
10. How registers are used in Buses?

SECTION-B

11. What are different logic Gates? Write its applications.
12. Explain with diagram about half adder.
13. What is Boolean Algebra? Write some theorems used in Boolean Algebra.
14. What is race condition in JK Flip Flop? How it can be removed?
15. Discuss the types of Instruction Format including memory, and register reference instructions.
16. Explain common Bus System and its types in Computer organization.

NOTE : Disclosure of Identity by writing Mobile No. or Making of passing request on any page of Answer Sheet will lead to UMC against the Student.