Total No. of Questions : 08

Total No. of Pages : 01

## BCA / DEP (Sem.-3) DIGITAL CIRCUITS & LOGIC DESIGN Subject Code : BSBC-303 M.Code : 10059 Date of Examination : 12-07-21

## Time : 2 Hrs.

Max. Marks : 60

## **INSTRUCTIONS TO CANDIDATES :**

1. Attempt any FIVE question(s), each question carries 12 marks.

- 1. Design and explain 3bit up down asynchronous counter.
- 2. Discuss various types of Logic Gates.
- 3. Write a note on K-Maps.
- 4. Discuss the working of SR flip flop.
- 5. Describe various error detecting and correcting codes.
- 6. Write a note on carry look ahead adder.
- 7. Explain with truth tables the difference between AND, OR, NAND, NOR and XOR gates.
- 8. Convert the following numbers into Binary number system :

ABCD(16), 7657(8), 9753(10), F4E6(16)

<u>Note</u>: Any student found attempting answer sheet from any other person(s), using incriminating material or involved in any wrong activity reported by evaluator shall be treated under UMC provisions.

Student found sharing the question paper(s)/answer sheet on digital media or with any other person or any organization/institution shall also be treated under UMC.

Any student found making any change/addition/modification in contents of scanned copy of answer sheet and original answer sheet, shall be covered under UMC provisions.