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Total No. of Pages : 01

Total No. of Questions : 08

BCA (Sem.-2)
COMPUTER SYSTEM ARCHITECTURE
Subject Code : BSBC-204
M.Code : 10053
Date of Examination : 02-07-21

Time : 2 Hrs.

Max. Marks : 60

INSTRUCTIONS TO CANDIDATES :

1. Attempt any FIVE question(s), each question carries 12 marks.

1. What is meant by Von Neumann Architecture? Compare SIMD and MIMD based architecture.
2. What is a micro operation? Into how many categories micro operations can be classified.
3. What is meant by instruction execution? How does timing & control take place in control unit for instruction execution? Illustrate through its block diagram.
4. What are register reference instructions? When are they identified? Explain with an example.
5. Give the block diagram of DMA controller. Why are the read and write control lines in a DMA controller bidirectional?
6. What do you mean by asynchronous data transfer? Explain strobe controller and hand shaking mechanism for asynchronous data transfer.
7. Show the basic architecture of mobile device. Give suitable examples.
8. What is associative mapping in cache? Explain how it is different from set associative mapping?

Note: Any student found attempting answer sheet from any other person(s), using incriminating material or involved in any wrong activity reported by evaluator shall be treated under UMC provisions.

Student found sharing the question paper(s)/answer sheet on digital media or with any other person or any organization/institution shall also be treated under UMC.

Any student found making any change/addition/modification in contents of scanned copy of answer sheet and original answer sheet, shall be covered under UMC provisions.