Roll No.							Total No. of Pages: 0

Total No. of Questions: 07

## B.Sc.(IT) (2015 & Onwards) (Sem. - 3) COMPUTER SYSTEM ARCHITECTURE

M Code: 74059 Subject Code: BSIT-301 Paper ID: [74059]

Time: 3 Hrs. Max. Marks: 60

## **INSTRUCTIONS TO CANDIDATES:**

- 1. SECTION-A is COMPULSORY consisting of TEN questions carrying TWO marks each.
- 2. SECTION-B contains SIX questions carrying TEN marks each and students have to attempt any FOUR questions.

## **SECTION A**

- 1. Explain the following:
  - a) Handshaking
  - b) Types of Buses
  - c) SISD and SIMD
  - d) Zero address instruction
  - e) Control Unit
  - f) Input Output Processor
  - g) Virtual Memory
  - h) Hit Ratio
  - i) LRU
  - j) Network Port

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## **SECTION B**

- 2. Differentiate between RISC and CISC. What are Interrupts and its types?
- 3. Discuss any five memory reference instructions with help of example.
- 4. Describe the working of DMA.
- 5. Write a note on types of micro operations.
- 6. Define mapping and explain various mapping techniques to map main memory to cache.
- 7. Explain about the evolution of computer.

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