Roll No. | | | | | | | | | | | | | **Total No. of Pages: 02**

Total No. of Questions: 07

BCA (2011 & Onward) (Sem. – 2)

COMPUTER SYSTEM ARCHITECTURE

M Code: 10053

Subject Code: BSBC-204

Paper ID: [B1116]

Time: 3 Hrs. **Max. Marks: 60**

INSTRUCTIONS TO CANDIDATES:

1. **SECTION-A is COMPULSORY consisting of TEN questions carrying TWO marks each.**
2. **SECTION-B contains SIX questions carrying TEN marks each and students has to attempt any FOUR questions.**

SECTION A

1. a) Differentiate the terms SISD, SIMD and MIMD. 02

   b) Define the term computer organization. How it is different from computer architecture. 02

   c) How data movement is done among register and memory? 02

   d) What is instruction format? Explain. 02

   e) Define control unit. What are its functions? 02

   f) Draw the flow chart for instruction cycle. 02

   g) What is register stack? Explain. 02

   h) What is memory mapped I/O? Explain. 02

   i) What is physical and logical address? Explain. 02

   j) What is window lite and Symbian? 02

SECTION B

2. What is micro operation? Explain different types of micro operation by taking suitable examples. 10

3. Write notes on the following:

   a) Stored program concept

   b) Common Bus System 10

4. What is an interrupt? What are its types? Draw and explain the working of interrupt cycle.

10

5. What is strobe and handshaking? Explain in detail. 10

6. What is associative mapping in cache? Explain how it is different from set associate memory mapping. 10

7. Explain the following:

   a) Ports and its types

   b) DMA Controller 10